



INFORMATION CITED BY APPLICANTS THAT MAY BE MATERIAL TO THE  
PROSECUTION OF THE SUBJECT APPLICATION

Applicants: Y. Arima et al. Attorney Docket No.: SUSU121795  
Application No.: 10/674,951 Art Unit: 2138 / Confirmation No.: 5298  
Filed: September 30, 2003 Examiner: J.P. Trimmings  
Title: INTEGRATED CIRCUIT DEVICE

U.S. PATENT DOCUMENTS

None

FOREIGN PATENT DOCUMENTS

*Examiner Cite Initial	No.	Document No.	Kind Code	Publication Date (mm/dd/yyyy)	Country	English	
						Abstract Provided	Translation Provided
JPT	F10	05-108496	A2	04/30/1993	JP	X	
JPT	F11	06-052070	A2	02/25/1994	JP	X	
JPT	F12	06-139153	A2	05/20/1994	JP	X	
JPT	F13	2002-196846	A2	07/12/2002	JP	X	

OTHER INFORMATION

(Including Author, Title, Date, Pertinent Pages, Etc.)

None

**Examiner**

**Date Considered**

/John P Trimmings/

02/12/2007

\*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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